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**JAN 03 2005**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Applicant Jaroslav Hynecsek

Docket Number: TI-36483

Serial No.: 10/633,993

Art Unit: 2826

Filed: 08/04/03

Examiner: Johannes P. Mondt

For: Clocked Barrier Virtual Phase Charge Coupled Device  
Image Sensor

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below:

Karen Vertz  
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<input type="checkbox"/> <b>CONTINUATION APP'N</b>	<input checked="" type="checkbox"/> <b>ELECTION</b>
<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
<b>NAME OF INVENTOR(S):</b> Jaroslav Hynecsek	
<b>TITLE OF INVENTION:</b> Clocked Barrier Virtual Phase Charge Coupled Device Image Sensor	
<b>TI FILE NO.:</b> <b>TI-36483</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED:</b> 1-3-05 <b>DUE:</b> 1-17-05 <b>ATTY/SECY:</b> AKS/kjv	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 10/633,993</b> <b>Filing Date:</b> 08/04/03	

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Texas Instruments Incorporated  
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

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ELECTION

This election is offered in response to the Examiner's restriction requirement mailed December 17, 2004.

Applicant hereby elects to pursue Species 2: Figure 3, pages 10-11, claims 11-17, without traversing the Examiner's restriction requirement.

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Respectfully submitted,



Alan K. Stewart  
Attorney for Applicant  
Registration No. 35,373